

REMARKS/ARGUMENTS

Claims 1, 4, 6-9, 11-29, 31-35, 37-52, and 54-57 are pending in the application. By this amendment, claims 1, 16-18, 21, 31, 37-38, 40, 42, and 46-48 are amended. Hence, claims 1, 4, 6-9, 11-29, 31-35, 37-52, and 54-57 remain pending in the application.

Applicant hereby requests further examination and reconsideration of the application, in view of the foregoing amendments.

In the Specification

Applicant has amended the specification to secure a substantial correspondence between the claims amended herein and the remainder of the specification. No new matter is presented.

In the Claims

In the Office Action of 01/11/2008, the Examiner rejected claims 1-9, 11-29, 31-35, and 37-39 under 35 U.S.C. 103(a) as being unpatentable over Hashimoto et al., US 6,983,374 (hereinafter, “Hashimoto”), and further in view of Muratani et al., US 7,194,090 (hereinafter, “Muratani”). Applicant respectfully traversed the Examiner’s rejections and amended a number of claims to more clearly distinguish the claimed invention from the prior art. Arguments in support of the traversals were provided in the foregoing Response C.

Subsequent to submittal of the Response C, the undersigned representative conducted an examiner interview of 05/29/2008 to orally discuss the cited references and to present arguments in support of allowance.

Two significant comments were understood during the interview. The first was that the Examiner did not believe that that claim language supported Applicant’s assertion that the recited operations were performed by a microprocessor. It was pointed out that the recited “computing device” and “device” would read upon other devices other than a microprocessor. Accordingly, Applicant has amended the claims to specifically recite “microprocessor” in lieu of a “computing device” or “device.”

The other significant comment understood from the interview was that the claim language did not clearly state that the recited cryptography unit was indeed part of the microprocessor, as discussed by the undersigned. Accordingly, the claims are amended herein to more clearly state that the recited cryptography unit is found within the recited microprocessor.

Applicant respectfully asserts that the arguments presented in the response of 04/11/2008 support all of the amendments made therein, and that the claims are indeed allowable in their form of 04/11/2008. However, to expedite the patent application process in a manner consistent with the PTO's Patent business Goals (PBG), 65 Fed. Reg. 54603 (September 8, 2000), Applicant has chosen to provide the amendments noted herein.

CONCLUSIONS

Applicant believes, in view of the amendments and arguments advanced above, that claims 1, 4, 6-9, 11-29, 31-35, 37-52, and 54-57 are in condition for allowance. Reconsideration of the rejections is requested, and allowance of the claims is solicited.

Applicant also notes that any amendments made by way of this response, and the observations contained herein, are made solely for the purpose of expediting the patent application process in a manner consistent with the PTO's Patent business Goals (PBG), 65 Fed. Reg. 54603 (September 8, 2000), and are furthermore made without prejudice to Applicant under this or any other jurisdictions. It is moreover asserted that insofar as any subject matter might otherwise be regarded as having been abandoned or effectively disclaimed by virtue of amendments made herein and/or incorporated in attachments submitted with this response, Applicants wishes to reserve the right and hereby provides notice of intent to restore such subject matter and/or file a continuation application in respect thereof.

Applicant earnestly requests that the Examiner contact the undersigned practitioner by telephone if the Examiner has any questions or suggestions concerning this amendment, the application, or allowance of any claims thereof.

Respectfully submitted,
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